

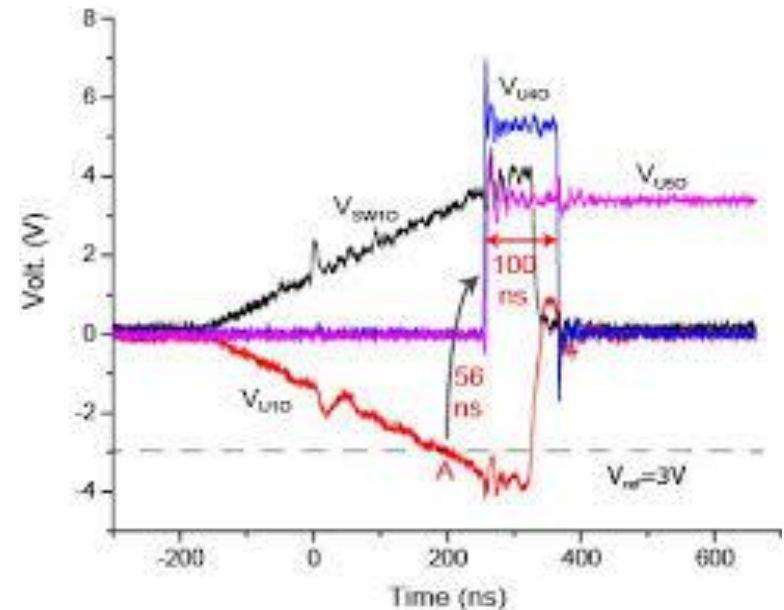
A compliance CC with nsec response time for ReRAM characterization

Background: ReRAM is an emerging technology in both Industrial and academic communities. Compliance current (CC) is a factor that can significantly influence ReRAM potential performance.

State of art apparatus propose response time for CC circuit between 100usec and msec. This slow response limit accuracy potential of preforming operation.

Project Description:

- Read Paper
- CC design
- SPICE Simulation
- Circuit implementation
- Performance comparison



Perquisites: Computer organization and Design

Recommended: Lab1

Host: VLSI Lab

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