ASIC² Project: ANN Training with ADC using differential pipelined topology

Background: memristor based neural networks techniques can be applied to analog to digital signal conversion. As any leaning system these networks have to be trained. In this project we shall train a network to act as a high speed ADC.

Project:
- Learn about ANNs and ADCs
- Characterize the 14-Bit, 1.25 GSPS AD9680 at lab
- Use ANN model and train it using ADC data
- Compare ADC to ANN

Prerequisites: LAB1
Recommended: TBD
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