Background: Just a few months ago were made available 22nm GlobalFoundries libraries of MTJ based on Everspin Technology. We started to build a basic block of Continuous Flow Multi-Threading. Now we want to make it real!

Project Description:

- Read Papers, Hands-on VLSI tools & Library integration
- Learn first project, Optimize Register design and prove feasibility
- Integrate new results to paper frame
- Be part of fabrication process

Perquisites: Computer organization and Design
Recommended: Lab1
Host: VLSI Lab
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